

+5V-Powered, Multichannel RS-232 Drivers/Receivers

General Description

The MAX220–MAX249 family of line drivers/receivers is intended for all EIA/TIA-232E and V.28/V.24 communications interfaces, particularly applications where \pm 12V is not available.

These parts are especially useful in battery-powered systems, since their low-power shutdown mode reduces power dissipation to less than 5μ W. The MAX225, MAX233, MAX235, and MAX245/MAX246/MAX247 use no external components and are recommended for applications where printed circuit board space is critical.

Applications

Portable Computers

- Low-Power Modems
- Interface Translation

Products. Inc.

Battery-Powered RS-232 Systems

AutoShutdown and UCSP are trademarks of Maxim Integrated

Multidrop RS-232 Networks

Next-Generation Device Features

- ♦ For Low-Voltage, Integrated ESD Applications MAX3222E/MAX3232E/MAX3237E/MAX3241E/ MAX3246E: +3.0V to +5.5V, Low-Power, Up to 1Mbps, True RS-232 Transceivers Using Four 0.1µF External Capacitors (MAX3246E Available in a UCSPTM Package)
- ♦ For Low-Cost Applications MAX221E: ±15kV ESD-Protected, +5V, 1µA, Single RS-232 Transceiver with AutoShutdown™

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PART	TEMP RANGE	PIN-PACKAGE
MAX220CPE+	0°C to +70°C	16 Plastic DIP
MAX220CSE+	0°C to +70°C	16 Narrow SO
MAX220CWE+	0°C to +70°C	16 Wide SO
MAX220C/D	0°C to +70°C	Dice*
MAX220EPE+	-40°C to +85°C	16 Plastic DIP
MAX220ESE+	-40°C to +85°C	16 Narrow SO
MAX220EWE+	-40°C to +85°C	16 Wide SO
MAX220EJE	-40°C to +85°C	16 CERDIP
MAX220MJE	-55°C to +125°C	16 CERDIP
MAX220MJE		16 CERDIP

Ordering Information

+Denotes a lead(Pb)-free/RoHS-compliant package.

*Contact factory for dice specifications.

Ordering Information continued at end of data sheet.

Selection Table

	Power	No. of		Nominal	SHDN	Rx		
Part	Supply	RS-232	No. of		& Three-	Active in		
Number	(V)	Drivers/Rx		(µF)	State	SHDN	(kbps)	Features
MAX220	+5	2/2	4	0.047/0.33	No	_	120	Ultra-low-power, industry-standard pinout
MAX222	+5	2/2	4	0.1	Yes	_	200	Low-power shutdown
MAX223 (MAX213)	+5	4/5	4	1.0 (0.1)	Yes	~	120	MAX241 and receivers active in shutdown
MAX225	+5	5/5	0	_	Yes	~	120	Available in SO
MAX230 (MAX200)	+5	5/0	4	1.0 (0.1)	Yes	_	120	5 drivers with shutdown
MAX231 (MAX201)	+5 and	2/2	2	1.0 (0.1)	No	_	120	Standard +5/+12V or battery supplies;
	+7.5 to +13.2							same functions as MAX232
MAX232 (MAX202)	+5	2/2	4	1.0 (0.1)	No	_	120 (64)	Industry standard
MAX232A	+5	2/2	4	0.1	No	_	200	Higher slew rate, small caps
MAX233 (MAX203)	+5	2/2	0	_	No	_	120	No external caps
MAX233A	+5	2/2	0	_	No	_	200	No external caps, high slew rate
MAX234 (MAX204)	+5	4/0	4	1.0 (0.1)	No	_	120	Replaces 1488
MAX235 (MAX205)	+5	5/5	0	_	Yes	_	120	No external caps
MAX236 (MAX206)	+5	4/3	4	1.0 (0.1)	Yes	_	120	Shutdown, three state
MAX237 (MAX207)	+5	5/3	4	1.0 (0.1)	No	_	120	Complements IBM PC serial port
MAX238 (MAX208)	+5	4/4	4	1.0 (0.1)	No	_	120	Replaces 1488 and 1489
MAX239 (MAX209)	+5 and	3/5	2	1.0 (0.1)	No		120	Standard +5/+12V or battery supplies;
	+7.5 to +13.2							single-package solution for IBM PC serial port
MAX240	+5	5/5	4	1.0	Yes	_	120	DIP or flatpack package
MAX241 (MAX211)	+5	4/5	4	1.0 (0.1)	Yes	_	120	Complete IBM PC serial port
MAX242	+5	2/2	4	0.1	Yes	~	200	Separate shutdown and enable
MAX243	+5	2/2	4	0.1	No	_	200	Open-line detection simplifies cabling
MAX244	+5	8/10	4	1.0	No	_	120	High slew rate
MAX245	+5	8/10	0	_	Yes	v	120	High slew rate, int. caps, two shutdown modes
MAX246	+5	8/10	0	_	Yes	~	120	High slew rate, int. caps, three shutdown modes
MAX247	+5	8/9	0	_	Yes	v	120	High slew rate, int. caps, nine operating modes
MAX248	+5	8/8	4	1.0	Yes	~	120	High slew rate, selective half-chip enables
MAX249	+5	6/10	4	1.0	Yes	~	120	Available in quad flatpack package

For pricing, delivery, and ordering information, please contact Maxim Direct at 1-888-629-4642, or visit Maxim's website at www.maximintegrated.com.

+5V-Powered, Multichannel RS-232 Drivers/Receivers

ABSOLUTE MAXIMUM RATINGS—MAX220/222/232A/233A/242/243

(Voltages referenced to GND.)

V _{CC} 0.3V to +6V
V+ (Note 1)(V _{CC} - 0.3V) to +14V
V- (Note 1)+0.3V to -14V
Input Voltages
TIN0.3V to (V _{CC} - 0.3V)
RIN (Except MAX220)±30V
RIN (MAX220)±25V
TOUT (Except MAX220) (Note 2)±15V
TOUT (MAX220)±13.2V
Output Voltages
TOUT±15V
ROUT0.3V to (V _{CC} + 0.3V)
Driver/Receiver Output Short Circuited to GNDContinuous
Continuous Power Dissipation ($T_A = +70^{\circ}C$)
16-Pin Plastic DIP (derate 10.53mW/°C above +70°C)842mW
18-Pin Plastic DIP (derate 11.11mW/°C above +70°C)889mW
20-Pin Plastic DIP (derate 8.00mW/°C above +70°C)440mW

16-Pin Narrow SO (derate 8.70mW/°C above +70°C)696mW 16-Pin Wide SO (derate 9.52mW/°C above +70°C)762mW
18-Pin Wide SO (derate 9.52mW/°C above +70°C)762mW
20-Pin Wide SO (derate 10.00mW/°C above +70°C)800mW
20-Pin SSOP (derate 8.00mW/°C above +70°C)
16-Pin CERDIP (derate 10.00mW/°C above +70°C)800mW
18-Pin CERDIP (derate 10.53mW/°C above +70°C)842mW
Operating Temperature Ranges
MAX2AC, MAX2C0°C to +70°C
MAX2AE, MAX2E40°C to +85°C
MAX2AM, MAX2M55°C to +125°C
Storage Temperature Range65°C to +160°C
Lead Temperature (soldering, 10s)+300°C
Soldering Temperature (reflow)
20 PDIP (P20M+1)+225°C
All other lead(Pb)-free packages+260°C
All other packages containing lead(Pb)+240°C

Note 1: For the MAX220, V+ and V- can have a maximum magnitude of 7V, but their absolute difference cannot exceed 13V. **Note 2:** Input voltage measured with TOUT in high-impedance state, V_{SHDN} or $V_{CC} = 0V$.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

ELECTRICAL CHARACTERISTICS—MAX220/222/232A/233A/242/243

(V_{CC} = +5V ±10%, C1–C4 = 0.1µF, MAX220, C1 = 0.047µF, C2–C4 = 0.33µF, T_A = T_{MIN} to T_{MAX}, unless otherwise noted.) (Note 3)

PARAMETER	CONDITIONS			ТҮР	MAX	UNITS	
RS-232 TRANSMITTERS	1						
Output Voltage Swing	All transmitter output	its loaded with 3k Ω to GND	±5	±8		V	
Input Logic-Low Voltage				1.4	0.8	V	
Input Logia High Voltage	All devices except	MAX220	2	1.4		v	
Input Logic-High Voltage	MAX220: V _{CC} = +5.	OV	2.4			v	
	All except MAX220	normal operation		5	40		
Logic Pullup/Input Current VSHDN = 0V, MAX220		= 0V, MAX222/MAX242, shutdown, 0			±1	μA	
	$V_{CC} = +5.5V, V_{\overline{SHDN}} = 0V, V_{OUT} = \pm 15V,$ MAX222/MAX242			±0.01	±10		
Output Leakage Current		$V_{OUT} = \pm 15V$		±0.01	±10	μΑ	
	$V_{CC} = V_{\overline{SHDN}} = 0V$	MAX220, $V_{OUT} = \pm 12V$			±25		
Data Rate				200	116	kbps	
Transmitter Output Resistance	$V_{\rm CC} = V + = V - = 0V$	$V_{OUT} = \pm 2V$	300	10M		Ω	
Output Short-Circuit Current	Vout = 0V	$V_{OUT} = 0V$	±7	±22		mA	
Output Short-Circuit Current	V001 = 0V	MAX220			±60	IIIA	
RS-232 RECEIVERS							
RS-232 Input Voltage Operating Range					±30	V	
no-232 input voltage Operating hange		MAX220			±25	v	
RS-232 Input Threshold Low	$V_{CC} = +5V$	All except MAX243 R2IN	0.8	1.3		V	
		MAX243 R2IN (Note 4)	-3			v	
RS-232 Input Threshold High	$V_{CC} = +5V$	All except MAX243 R2IN		1.8	2.4	V	
	MAX243 R2IN (Note 4)			-0.5	-0.1	v	

+5V-Powered, Multichannel RS-232 Drivers/Receivers

ELECTRICAL CHARACTERISTICS—MAX220/222/232A/233A/242/243 (continued)

 $(V_{CC} = +5V \pm 10\%, C1-C4 = 0.1\mu$ F, MAX220, C1 = 0.047 μ F, C2-C4 = 0.33 μ F, T_A = T_{MIN} to T_{MAX}, unless otherwise noted.) (Note 3)

PARAMETER		CC	ONDITIONS	MIN	ТҮР	MAX	UNITS	
	All except MAX220/MAX243, V_{CC} = +5V, no hysteresis in shutdown			0.2	0.5	1.0		
RS-232 Input Hysteresis	MAX220				0.3		V	
	MAX243				1			
	T 0500 (1		20)	3	5	7		
RS-232 Input Resistance	$T_{A} = +25^{\circ}C$ (N	VIAX22	20)	3	5	7	kΩ	
	I _{OUT} = 3.2mA				0.2	0.4	M	
TTL/CMOS Output Voltage Low	I _{OUT} = 1.6mA	(MAX	220)			0.4	V	
TTL/CMOS Output Voltage High	I _{OUT} = -1.0mA	١		3.5	V _{CC} - 0.	2	V	
TTL/CMOS Output Short Circuit Current	Sourcing Vou	r = Vg	ND	-2	-10		m ^	
TTL/CMOS Output Short-Circuit Current	Sinking VOUT	= Vcc		10	30		mA	
TTL/CMOS Output Leakage Current	$V_{\overline{SHDN}} = V_{CC} (MAX222), 0V \leq 100$		$T = V_{CC} (V_{\overline{SHDN}} = 0V \text{ for})$ $T \leq V_{CC}$		±0.05	±10	μA	
EN Input Threshold Low	MAX242				1.4	0.8	V	
EN Input Threshold High	MAX242			2.0	1.4		V	
Supply Voltage Range				4.5		5.5	V	
		MAX220			0.5	2		
V _{CC} Supply Current (V _{SHDN} = V _{CC}), Figures 5, 6, 11, 19	No load		(222/MAX232A/MAX233A/ (242/MAX243		4	10		
	3kΩ load MAX220 both inputs MAX222/MAX232A/MAX233A/ MAX242/MAX243 MAX242/MAX243		<220		12		mA	
					15			
	MAX222/ T _A = MAX242 T _A =		= +25°C		0.1	10		
			= 0°C to +70°C		2	50	μΑ	
Shutdown Supply Current			= -40°C to +85°C		2	50		
			= -55°C to +125°C		35	100		
SHDN Input Leakage Current	MAX222/MAX	242				±1	μA	
SHDN Threshold Low	MAX222/MAX	242			1.4	0.8	V	
SHDN Threshold High	MAX222/MAX	242		2.0	1.4		V	
Transition Slow Pate	$\begin{array}{l} C_L = 50 \mbox{pF} to 2500 \mbox{pF}, \\ R_L = 3 \mbox{k} \Omega \ to \ 7 \mbox{k} \Omega, \\ V_{CC} = +5 \mbox{V}, \ T_A = \\ +25 \ ^\circ \mbox{C}, \ measured \\ from \ +3 \ V \ to \ -3 \ V \ or \\ -3 \ V \ to \ +3 \ V \end{array}$		MAX222/MAX232A/ MAX233/MAX242/MAX243	6	12	30	Mue	
Transition Slew Rate			MAX220	1.5	3	30.0	V/µs	
	t _{PHLT} , Figure 1		MAX222/MAX232A/ MAX233/MAX242/MAX243		1.3	3.5		
Transmitter Propagation Delay TLL to			MAX220		4	10	116	
RS-232 (Normal Operation)	t _{PLHT} , Figure 1		MAX222/MAX232A/ MAX233/MAX242/MAX243		1.5	3.5	μs	
			MAX220		5	10		

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ELECTRICAL CHARACTERISTICS—MAX220/222/232A/233A/242/243 (continued)

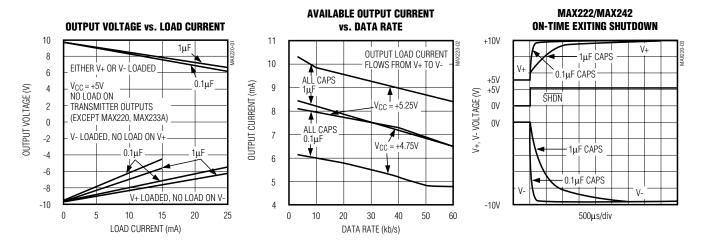
 $(V_{CC} = +5V \pm 10\%, C1-C4 = 0.1\mu$ F, MAX220, C1 = 0.047 μ F, C2-C4 = 0.33 μ F, T_A = T_{MIN} to T_{MAX}, unless otherwise noted.) (Note 3)

PARAMETER		CONDITIONS	MIN	ТҮР	MAX	UNITS	
	t _{PHLR} , Figure 2	PHLR, Figure 2 MAX222/MAX232A/MAX233/		0.5	1		
Receiver Propagation Delay RS-232 to		MAX220		0.6	3]	
TLL (Normal Operation)	t _{PLHR} , Figure 2	MAX222/MAX232A/MAX233/ MAX242/MAX243		0.6	1	μs	
		MAX220		0.8	3		
Receiver Propagation Delay RS-232 totpHLS, FigureTLL (Shutdown)tpHLS, Figure		MAX242		0.5	10		
		MAX242		2.5	10	μs	
Receiver-Output Enable Time	t _{ER}	MAX242, Figure 3		125	500	ns	
Receiver-Output Disable Time	tDR	MAX242, Figure 3		160	500	ns	
Transmitter-Output Enable Time (SHDN Goes High)	tet	MAX222/MAX242, 0.1µF caps (includes charge-pump start-up), Figure 4		250		μs	
Transmitter-Output Disable Time (SHDN Goes Low)	t _{DT}	MAX222/MAX242, 0.1µF caps, Figure 4		600		ns	
Transmitter + to - Propagation Delay Difference (Normal Operation)	tphlt - tplht	MAX222/MAX232A/MAX233/ MAX242/MAX243		300		ns	
Difference (Normal Operation)		MAX220		2000		1	
Receiver + to - Propagation Delay Difference (Normal Operation)	tphlr - tplhr	MAX222/MAX232A/MAX233/ MAX242/MAX243		100		ns	
		MAX220		225			

Note 3: All units are production tested at hot. Specifications over temperature are guaranteed by design. **Note 4:** MAX243 R2OUT is guaranteed to be low when R2IN \ge 0V or is unconnected.

Typical Operating Characteristics

MAX220/MAX222/MAX232A/MAX233A/MAX242/MAX243



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ABSOLUTE MAXIMUM RATINGS—MAX223/MAX230–MAX241

(Voltages referenced to GND.)
V _{CC} 0.3V to +6V
V+(V _{CC} - 0.3V) to +14V
V+0.3V to -14V
Input Voltages
TIN0.3V to (V _{CC} + 0.3V)
RIN±30V
Output Voltages
TOUT(V+ + 0.3V) to (V 0.3V)
ROUT0.3V to (V _{CC} + 0.3V)
Short-Circuit Duration, TOUT to GNDContinuous
Continuous Power Dissipation ($T_A = +70^{\circ}C$)
14-Pin Plastic DIP (derate 10.00mW/°C above +70°C)800mW
16-Pin Plastic DIP (derate 10.53mW/°C above +70°C)842mW
20-Pin Plastic DIP (derate 11.11mW/°C above +70°C)889mW
24-Pin Narrow Plastic DIP
(derate 13.33mW/°C above +70°C)1.07W 24-Pin Plastic DIP (derate 9.09mW/°C above +70°C)500mW
16-Pin Wide SO (derate 9.52mW/°C above +70°C)762mW
20-Pin Wide SO (derate 10.00mW/°C above +70°C)800mW
24-Pin Wide SO (derate 11.76mW/°C above +70°C)941mW
24^{-1} in white 50 (derate 11.70mW) C above ±70 C)

28-Pin Wide SO (derate 12.50mW/°C above +70°C)1W 44-Pin Plastic FP (derate 11.11mW/°C above +70°C)889mW 14-Pin CERDIP (derate 9.09mW/°C above +70°C)727mW 16-Pin CERDIP (derate 10.00mW/°C above +70°C)800mW 20-Pin CERDIP (derate 11.11mW/°C above +70°C)889mW 24-Pin Narrow CERDIP

(derate 12.50mW/°C above +70°C)1W
24-Pin Sidebraze (derate 20.0mW/°C above +70°C)1.6W
28-Pin SSOP (derate 9.52mW/°C above +70°C)
Operating Temperature Ranges
MAX2 C0°C to +70°C
MAX2 E40°C to +85°C
MAX2 M55°C to +125°C
Storage Temperature Range65°C to +160°C
Lead Temperature (soldering, 10s)+300°C
Soldering Temperature (reflow)
20 PDIP (P20M+1)+225°C
24 PDIP (P24M-1)+225°C
All other lead(Pb)-free packages+260°C
All other packages containing lead(Pb)+240°C

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

ELECTRICAL CHARACTERISTICS—MAX223/MAX230–MAX241

 $(MAX223/230/232/234/236/237/238/240/241, V_{CC} = +5V \pm 10\%; MAX233/MAX235, V_{CC} = +5V \pm 5\%, C1-C4 = 1.0\mu\text{F}; MAX231/MAX239, V_{CC} = +5V \pm 10\%; V + = +7.5V \text{ to } +13.2V; T_A = T_{MIN} \text{ to } T_{MAX}; \text{ unless otherwise noted.}) (Note 5)$

PARAMETER		MIN	TYP	MAX	UNITS		
Output Voltage Swing	All transmitter	outputs loaded with $3k\Omega$ to ground	±5.0	±7.3		V	
		MAX232/233		5	10		
V _{CC} Supply Current	No load, T _A = +25°C	MAX223/230/234-238/240/241		7	15	mA	
	14 - 120 0	MAX231/239		0.4	1		
V+ Supply Current		MAX231		1.8	5	– mA	
		MAX239		5	15		
Shutdown Supply Current T _A = +25°C		MAX223		15	50		
		MAX230/235/236/240/241		1	10	- μΑ	
Input Logic-Low Voltage	TIN, EN, SHD			0.8	V		
	TIN		2.0				
Input Logic-High Voltage	EN, <u>SHDN</u> (MAX223); EN, SHDN (MAX230/235/236/240/241)		2.4			V	
Logic Pullup Current	$V_{TIN} = 0V$	/ _{TIN} = 0V		1.5	200	μA	
Receiver Input Voltage Operating Range			-30		+30	V	

+5V-Powered, Multichannel RS-232 Drivers/Receivers

ELECTRICAL CHARACTERISTICS—MAX223/MAX230–MAX241 (continued)

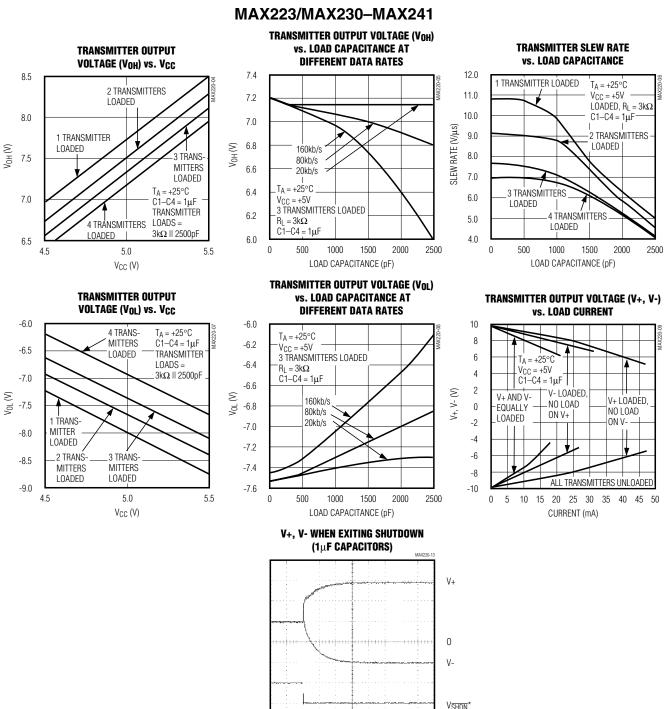
 $(MAX223/230/232/234/236/237/238/240/241, V_{CC} = +5V \pm 10\%; MAX233/MAX235, V_{CC} = +5V \pm 5\%, C1-C4 = 1.0\mu\text{F}; MAX231/MAX239, V_{CC} = +5V \pm 10\%; V_{+} = +7.5V \text{ to } +13.2V; T_{A} = T_{MIN} \text{ to } T_{MAX}; \text{ unless otherwise noted.}) (Note 5)$

PARAMETER		CONDITIONS		MIN	ТҮР	MAX	UNITS	
RS-232 Input Logic-Low Voltage	$\label{eq:TA} \begin{array}{l} \mbox{Normal operation} \\ \mbox{V}_{\overline{SHDN}} = +5V \mbox{(MAX223)} \\ \mbox{V}_{SHDN} = 0V \mbox{(MAX235/236/240/241)} \end{array}$			0.8	1.2		v	
no-232 Input Logic-Low Voltage	$V_{CC} = +5V$	Shutdown (MAX22 V _{SHDN} = 0V, V _{EN} = +5V (R4IN	,	0.6	1.5			
RS-232 Input Logic-High Voltage	T _A = +25°C,	Normal operation V <u>SHDN</u> = 5V (MA V _{SHDN} = 0V (MA	X223) X235/236/240/241)		1.7	2.4	v	
no-232 input Logic-nigh Voltage	$V_{CC} = +5V$	Shutdown (MAX22 V _{SHDN} = 0V, V _{EN} = +5V (R4IN	,		1.5	2.4		
RS-232 Input Hysteresis	$V_{CC} = +5V$, no hy	ysteresis in shutdow	'n	0.2	0.5	1.0	V	
RS-232 Input Resistance	$T_A = +25^{\circ}C, V_{CC}$	= +5V		3	5	7	kΩ	
TTL/CMOS Output Voltage Low	I _{OUT} = 1.6mA (M	DUT = 1.6mA (MAX231/232/233, I _{OUT} = 3.2mA)				0.4	V	
TTL/CMOS Output Voltage High	I _{OUT} = -1mA				V _{CC} - 0.4		V	
TTL/CMOS Output Leakage Current	$\begin{array}{l} 0V \leq R_{OUT} \leq V_{CC}; V_{EN} = 0V \; (MAX223); \\ V_{\overline{EN}} = V_{CC} \; (MAX235-241) \end{array}$				±0.05	±10	μA	
Receiver Output Enable Time	Normal MAX223				600		20	
Neceiver Output Enable Time	operation	MAX235/236/239/2	240/241		400		ns	
Receiver Output Disable Time	Normal MAX223				900		- ns	
Neceiver Output Disable fille	operation	MAX235/236/239/2		250		115		
	RS-232 IN to	Normal operation			0.5	10		
Propagation Delay	TTL/CMOS OUT,	V _{SHDN} = 0V	t _{PHLS}		4	40	μs	
	C _L = 150pF	(MAX223)	t _{PLHS}		6	40		
Transition Degion Slow Date	$\begin{array}{l} \mbox{MAX223/MAX230/MAX234-241, T_A = +25^{\circ}C, V_{CC} = +5V, \\ \mbox{R}_L = 3k\Omega \mbox{ to } 7k\Omega, C_L = 50\mbox{pF} \mbox{ to } 2500\mbox{pF}, \mbox{ measured from} \\ +3V \mbox{ to } -3V \mbox{ or } -3V \mbox{ to } +3V \\ \mbox{MAX231/MAX232/MAX233, T_A = +25^{\circ}C, V_{CC} = +5V, \\ \mbox{R}_L = 3k\Omega \mbox{ to } 7k\Omega, C_L = 50\mbox{pF} \mbox{ to } 2500\mbox{pF}, \mbox{ measured from} \\ +3V \mbox{ to } -3V \mbox{ or } -3V \mbox{ to } +3V \end{array}$		3	5.1	30			
Transition Region Slew Rate					4	30	- V/µs	
Transmitter Output Resistance	$V_{CC} = V_{+} = V_{-} = 0V, V_{OUT} = \pm 2V$			300			Ω	
Transmitter Output Short-Circuit Current					±10		mA	

Note 5: All units are production tested at hot except for the MAX240, which is production tested at $T_A = +25^{\circ}C$. Specifications over temperature are guaranteed by design.

+5V-Powered, Multichannel RS-232 Drivers/Receivers

Typical Operating Characteristics



*SHUTDOWN POLARITY IS REVERSED

500ms/div

+5V-Powered, Multichannel RS-232 Drivers/Receivers

ABSOLUTE MAXIMUM RATINGS—MAX225/MAX244–MAX249

(Voltages referenced to GND.)	Continuous Power Dissipation ($T_A = +70^{\circ}C$)
Supply Voltage (V _{CC})0.3V to +6V	28-Pin Wide SO (derate 12.50mW/°C above +70°C)1W
Input Voltages	40-Pin Plastic DIP (derate 11.11mW/°C above +70°C)611mW
TIN, ENA, ENB, ENR, ENT, ENRA,	44-Pin PLCC (derate 13.33mW/°C above +70°C)1.07W
ENRB, ENTA, ENTB0.3V to (V _{CC} + 0.3V)	Operating Temperature Ranges
RIN±25V	MAX225C, MAX24_C0°C to +70°C
TOUT (Note 6)±15V	MAX225E, MAX24_E40°C to +85°C
ROUT0.3V to (V _{CC} + 0.3V)	Storage Temperature Range65°C to +160°C
Short Circuit Duration (one output at a time)	Lead Temperature (soldering, 10s))+300°C
TOUT to GNDContinuous	Soldering Temperature (reflow)
ROUT to GNDContinuous	40 PDIP (P40M-2)+225°C
	All other lead(Pb)-free packages+260°C
	All other packages containing lead(Pb)+240°C

Note 6: Input voltage measured with transmitter output in a high-impedance state, shutdown, or $V_{CC} = 0V$.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

ELECTRICAL CHARACTERISTICS—MAX225/MAX244–MAX249

(MAX225, V_{CC} = +5.0V ±5%; MAX244–MAX249, V_{CC} = +5.0V ±10%, external capacitors C1–C4 = 1 μ F; T_A = T_{MIN} to T_{MAX}; unless otherwise noted.) (Note 7)

PARAMETER	CONDITIONS		MIN	ТҮР	MAX	UNITS
RS-232 TRANSMITTERS	1		-1		ı	
Input Logic-Low Voltage				1.4	0.8	V
Input Logic-High Voltage			2	1.4		V
Logic Pullup/Input Current	Tables 1a-1d	Normal operation		10	50	
Logic Fullup/Input Current	Tables Ta-Tu	Shutdown		±0.01	±1	μA
Data Rate	Tables 1a-1d, r	normal operation		120	64	kbps
Output Voltage Swing	All transmitter o	utputs loaded with $3k\Omega$ to GND	±5	±7.5		V
Output Lackage Oursent (Chutdows)	Tables 1a-1d	VENA, VENB, VENT, VENTA, VENTB = VCC, VOUT = ±15V		±0.01	±25	
Output Leakage Current (Shutdown)	Tables Ta-To	$V_{CC} = 0V,$ $V_{OUT} = \pm 15V$		±0.01	±25	μA
Transmitter Output Resistance	$V_{CC} = V + = V -$	= 0V, V_{OUT} = ±2V (Note 8)	300	10M		Ω
Output Short-Circuit Current	V _{OUT} = 0V		±7	±30		mA
RS-232 RECEIVERS						
RS-232 Input Voltage Operating Range					±25	V
RS-232 Input Logic-Low Voltage	$V_{CC} = +5V$		0.8	1.3		V
RS-232 Input Logic-High Voltage	$V_{\rm CC} = +5V$			1.8	2.4	V
RS-232 Input Hysteresis	$V_{\rm CC} = +5V$		0.2	0.5	1.0	V
RS-232 Input Resistance			3	5	7	kΩ
TTL/CMOS Output Voltage Low	$I_{OUT} = 3.2 \text{mA}$			0.2	0.4	V
TTL/CMOS Output Voltage High	I _{OUT} = -1.0mA		3.5	V _{CC} - 0.2		V
TTL /CMOC Output Chart Circuit Current	Sourcing V _{OUT} = V _{GND}		-2	-10		
TTL/CMOS Output Short-Circuit Current	Sinking V _{OUT} = V _{CC}		10	30		mA
TTL/CMOS Output Leakage Current	Normal operation, outputs disabled, Tables 1a–1d, $0V \le V_{OUT} \le V_{CC}$, V_{ENR} = V _{CC}			±0.05	±0.10	μΑ

+5V-Powered, Multichannel RS-232 Drivers/Receivers

ELECTRICAL CHARACTERISTICS—MAX225/MAX244–MAX249 (continued)

(MAX225, V_{CC} = +5.0V ±5%; MAX244–MAX249, V_{CC} = +5.0V ±10%, external capacitors C1–C4 = 1 μ F; T_A = T_{MIN} to T_{MAX}; unless otherwise noted.) (Note 7)

PARAMETER	CONDITIONS			TYP	MAX	UNITS
POWER SUPPLY AND CONTROL LO	GIC					1
		MAX225	4.75		5.25	V
Supply Voltage Range		MAX244-MAX249	4.5		5.5	
		MAX225		10	20	
V _{CC} Supply Current	No load	MAX244–MAX249		11	30	
(Normal Operation)	$3k\Omega$ loads on	MAX225		40		- mA
	all outputs	MAX244-MAX249		57		
Chutdown Supply Current	$T_A = +25^{\circ}C$			8	25	
Shutdown Supply Current	$T_A = T_{MIN}$ to T_I	MAX			50	- μΑ
	Leakage curre	nt			±1	μA
Control Input	Logic-low volta	ige		1.4	0.8	V
	Logic-high volt	age	2.4	1.4		
AC CHARACTERISTICS	•					
Transition Slew Rate		500pF, R _L = $3k\Omega$ to $7k\Omega$, V _{CC} = +5V, easured from +3V to -3V or -3V to +3V	5	10	30	V/µs
Transmitter Propagation Delay TLL to RS-232 (Normal Operation)	t _{PHLT} , Figure 1			1.3	3.5	110
	t _{PLHT} , Figure 1		1.5	3.5	- µs	
Receiver Propagation Delay	t _{PHLR} , Figure 2			0.6	1.5	- µs
TLL to RS-232 (Normal Operation)	t _{PLHR} , Figure 2		0.6	1.5	μ5	
Receiver Propagation Delay	tPHLS, Figure 2			0.6	10	
TLL to RS-232 (Low-Power Mode)	t _{PLHS} , Figure 2			3.0	10	μs
Transmitter + to - Propagation Delay Difference (Normal Operation)	tphlt - tplht			350		ns
Receiver + to - Propagation Delay Difference (Normal Operation)	tphlr - tplhr			350		ns
Receiver-Output Enable Time	t _{ER} , Figure 3			100	500	ns
Receiver-Output Disable Time	t _{DR} , Figure 3			100	500	ns
Transmitter Enable Time	+	MAX246–MAX249 (excludes charge-pump startup)		5		μs
Transmiller Enable Time	tet	MAX225/MAX245–MAX249 (includes charge-pump startup)		10		ms
Transmitter Disable Time	t _{DT} , Figure 4			100		ns

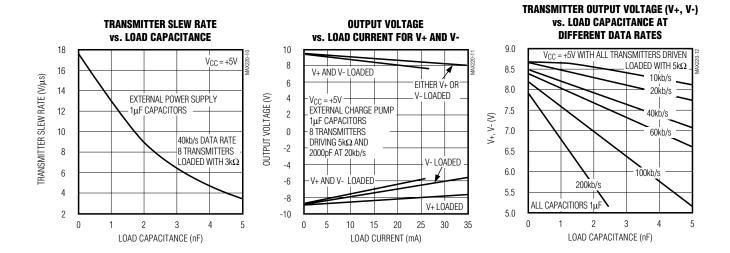
Note 7: All units production tested at hot. Specifications over temperature are guaranteed by design.

Note 8: The 300Ω minimum specification complies with EIA/TIA-232E, but the actual resistance when in shutdown mode or V_{CC} = 0V is $10M\Omega$ as is implied by the leakage specification.

+5V-Powered, Multichannel RS-232 Drivers/Receivers

Typical Operating Characteristics

MAX225/MAX244–MAX249



+5V-Powered, Multichannel RS-232 Drivers/Receivers

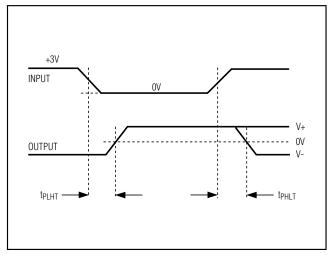


Figure 1. Transmitter Propagation-Delay Timing

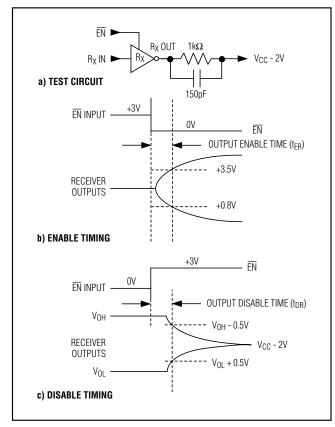


Figure 3. Receiver-Output Enable and Disable Timing

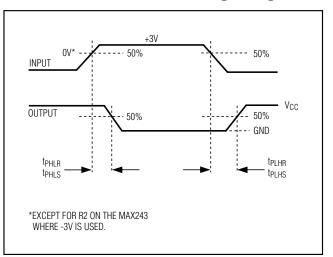


Figure 2. Receiver Propagation-Delay Timing

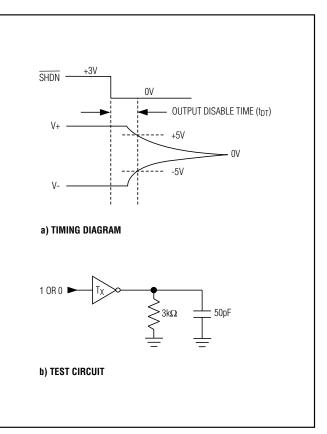


Figure 4. Transmitter-Output Disable Timing

Test Circuits/Timing Diagrams

+5V-Powered, Multichannel RS-232 Drivers/Receivers

Control Pin Configuration Tables

Table 1a. MAX245 Control Pin Configurations

ENT	ENR	OPERATION STATUS	TRANSMITTERS	RECEIVERS
0	0	Normal Operation	All Active	All Active
0	1	Normal Operation	All Active	All High-Z
1	0	Shutdown	All High-Z	All Low-Power Receive Mode
1	1	Shutdown	All High-Z	All High-Z

Table 1b. MAX245 Control Pin Configurations

ENT	ENR	OPERATION	TRANS	TRANSMITTERS		EIVERS		
		STATUS	TA1–TA4	TB1–TB4	RA1–RA5	RB1–RB5		
0	0	Normal Operation	All Active	All Active	All Active	All Active		
0	1	Normal Operation	All Active	All Active	RA1–RA4 High-Z, RA5 Active	RB1–RB4 High-Z, RB5 Active		
1	0	Shutdown	All High-Z	All High-Z	All Low-Power Receive Mode	All Low-Power Receive Mode		
1	1	Shutdown	All High-Z	All High-Z	RA1–RA4 High-Z, RA5 Low-Power Receive Mode	RB1–RB4 High-Z, RB5 Low-Power Receive Mode		

Table 1c. MAX246 Control Pin Configurations

ENA	ENB	OPERATION	OPERATION TRANSMITTERS		RECEIVERS		
ENA	END	STATUS	TA1–TA4	TB1–TB4	RA1–RA5	RB1–RB5	
0	0	Normal Operation	All Active	All Active	All Active	All Active	
0	1	Normal Operation	All Active	All High-Z	All Active	RB1–RB4 High-Z, RB5 Active	
1	0	Shutdown	All High-Z	All Active	RA1–RA4 High-Z, RA5 Active	All Active	
1	1	Shutdown	All High-Z	All High-Z	RA1–RA4 High-Z, RA5 Low-Power Receive Mode	RB1–RB4 High-Z, RA5 Low-Power Receive Mode	

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						TRANS	IITTERS	REC	EIVERS
ENTA	ENTB	ENRA	ENRB	OPERATION	MAX247	TA1–TA4	TB1–TB4	RA1–RA4	RB1–RB5
ENIA	ENIB	ENKA	ENKB	STATUS	MAX248	TA1–TA4	TB1–TB4	RA1–RA4	RB1–RB4
					MAX249	TA1–TA3	TB1–TB3	RA1–RA5	RB1–RB5
0	0	0	0	Normal Operation		All Active	All Active	All Active	All Active
0	0	0	1	Normal Operation		All Active	All Active	All Active	All High-Z, except RB5 stays active on MAX247
0	0	1	0	Normal Operation		All Active	All Active	All High-Z	All Active
0	0	1	1	Normal Operation		All Active	All Active	All High-Z	All High-Z, except RB5 stays active on MAX247
0	1	0	0	Normal Operation		All Active	All High-Z	All Active	All Active
0	1	0	1	Normal Operation		All Active	All High-Z	All Active	All High-Z, except RB5 stays active on MAX247
0	1	1	0	Normal Operation		All Active	All High-Z	All High-Z	All Active
0	1	1	1	Normal Operation		All Active	All High-Z	All High-Z	All High-Z, except RB5 stays active on MAX247
1	0	0	0	Normal Operation		All High-Z	All Active	All Active	All Active
1	0	0	1	Normal Operation		All High-Z	All Active	All Active	All High-Z, except RB5 stays active on MAX247
1	0	1	0	Normal Operation		All High-Z	All Active	All High-Z	All Active
1	0	1	1	Normal Operation		All High-Z	All Active	All High-Z	All High-Z, except RB5 stays active on MAX247
1	1	0	0	Shutdown		All High-Z	All High-Z	Low-Power Receive Mode	Low-Power Receive Mode
1	1	0	1	Shutdown		All High-Z	All High-Z	Low-Power Receive Mode	All High-Z, except RB5 stays active on MAX247
1	1	1	0	Shutdown		All High-Z	All High-Z	All High-Z	Low-Power Receive Mode
1	1	1	1	Shutdown		All High-Z	All High-Z	All High-Z	All High-Z, except RB5 stays active on MAX247

Table 1d. MAX247/MAX248/MAX249 Control Pin Configurations

+5V-Powered, Multichannel RS-232 Drivers/Receivers

Detailed Description

The MAX220–MAX249 contain four sections: dual charge-pump DC-DC voltage converters, RS-232 drivers, RS-232 receivers, and receiver and transmitter enable control inputs.

Dual Charge-Pump Voltage Converter The MAX220–MAX249 have two internal charge-pumps that convert +5V to ±10V (unloaded) for RS-232 driver operation. The first converter uses capacitor C1 to double the +5V input to +10V on C3 at the V+ output. The

second converter uses capacitor C2 to invert +10V to -10V on C4 at the V- output.

A small amount of power may be drawn from the +10V (V+) and -10V (V-) outputs to power external circuitry (see the *Typical Operating Characteristics* section), except on the MAX225 and MAX245–MAX247, where these pins are not available. V+ and V- are not regulated, so the output voltage drops with increasing load current. Do not load V+ and V- to a point that violates the minimum \pm 5V EIA/TIA-232E driver output voltage when sourcing current from V+ and V- to external circuitry.

When using the shutdown feature in the MAX222, MAX225, MAX230, MAX235, MAX236, MAX240, MAX241, and MAX245–MAX249, avoid using V+ and Vto power external circuitry. When these parts are shut down, V- falls to 0V, and V+ falls to +5V. For applications where a +10V external supply is applied to the V+ pin (instead of using the internal charge pump to generate +10V), the C1 capacitor must not be installed and the SHDN pin must be connected to V_{CC}. This is because V+ is internally connected to V_{CC} in shutdown mode.

RS-232 Drivers

The typical driver output voltage swing is ±8V when loaded with a nominal 5k Ω RS-232 receiver and V_{CC} = +5V. Output swing is guaranteed to meet the EIA/TIA-232E and V.28 specification, which calls for ±5V minimum driver output levels under worst-case conditions. These include a minimum 3k Ω load, V_{CC} = +4.5V, and maximum operating temperature. Unloaded driver output voltage ranges from (V+ -1.3V) to (V- +0.5V).

Input thresholds are both TTL and CMOS compatible. The inputs of unused drivers can be left unconnected since $400k\Omega$ input pullup resistors to V_{CC} are built in (except for the MAX220). The pullup resistors force the outputs of unused drivers low because all drivers invert. The internal input pullup resistors typically source 12µA, except in shutdown mode where the pullups are disabled. Driver outputs turn off and enter a high-impedance state—where leakage current is typically microamperes (maximum 25µA)—when in shutdown

mode, in three-state mode, or when device power is removed. Outputs can be driven to $\pm 15V$. The power-supply current typically drops to 8µA in shutdown mode. The MAX220 does not have pullup resistors to force the outputs of the unused drivers low. Connect unused inputs to GND or V_{CC}.

The MAX239 has a receiver three-state control line, and the MAX223, MAX225, MAX235, MAX236, MAX240, and MAX241 have both a receiver three-state control line and a low-power shutdown control. Table 2 shows the effects of the shutdown control and receiver threestate control on the receiver outputs.

The receiver TTL/CMOS outputs are in a high-impedance, three-state mode whenever the three-state enable line is high (for the MAX225/MAX235/MAX236/MAX239– MAX241), and are also high-impedance whenever the shutdown control line is high.

When in low-power shutdown mode, the driver outputs are turned off and their leakage current is less than 1µA with the driver output pulled to ground. The driver output leakage remains less than 1µA, even if the transmitter output is backdriven between 0V and (V_{CC} + 6V). Below -0.5V, the transmitter is diode clamped to ground with 1k Ω series impedance. The transmitter is also zener clamped to approximately V_{CC} + 6V, with a series impedance of 1k Ω .

The driver output slew rate is limited to less than 30V/µs as required by the EIA/TIA-232E and V.28 specifications. Typical slew rates are 24V/µs unloaded and 10V/µs loaded with 3Ω and 2500pF.

RS-232 Receivers

EIA/TIA-232E and V.28 specifications define a voltage level greater than 3V as a logic 0, so all receivers invert. Input thresholds are set at 0.8V and 2.4V, so receivers respond to TTL level inputs as well as EIA/TIA-232E and V.28 levels.

The receiver inputs withstand an input overvoltage up to $\pm 25V$ and provide input terminating resistors with

Table 2. Th	hree-State Co	ontrol of Recei	vers
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PART	SHDN	SHDN	EN	EN(R)	RECEIVERS
MAX223		Low High High	X Low High	_	High Impedance Active High Impedance
MAX225			_	Low High	High Impedance Active
MAX235 MAX236 MAX240	Low Low High		_	Low High X	High Impedance Active High Impedance

+5V-Powered, Multichannel RS-232 Drivers/Receivers

nominal 5k Ω values. The receivers implement Type 1 interpretation of the fault conditions of V.28 and EIA/TIA-232E.

The receiver input hysteresis is typically 0.5V with a guaranteed minimum of 0.2V. This produces clear output transitions with slow-moving input signals, even with moderate amounts of noise and ringing. The receiver propagation delay is typically 600ns and is independent of input swing direction.

Low-Power Receive Mode

The low-power receive mode feature of the MAX223, MAX242, and MAX245–MAX249 puts the IC into shutdown mode but still allows it to receive information. This is important for applications where systems are periodically awakened to look for activity. Using low-power receive mode, the system can still receive a signal that will activate it on command and prepare it for communication at faster data rates. This operation conserves system power.

Negative Threshold—MAX243

The MAX243 is pin compatible with the MAX232A, differing only in that RS-232 cable fault protection is removed on one of the two receiver inputs. This means that control lines such as CTS and RTS can either be driven or left unconnected without interrupting communication. Different cables are not needed to interface with different pieces of equipment.

The input threshold of the receiver without cable fault protection is -0.8V rather than +1.4V. Its output goes positive only if the input is connected to a control line that is actively driven negative. If not driven, it defaults to the 0 or "OK to send" state. Normally, the MAX243's other receiver (+1.4V threshold) is used for the data line (TD or RD), while the negative threshold receiver is connected to the control line (DTR, DTS, CTS, RTS, etc.).

Other members of the RS-232 family implement the optional cable fault protection as specified by EIA/TIA-232E specifications. This means a receiver output goes high whenever its input is driven negative, left unconnected, or shorted to ground. The high output tells the serial communications IC to stop sending data. To avoid this, the control lines must either be driven or connected with jumpers to an appropriate positive voltage level.

Shutdown—MAX222-MAX242

On the MAX222, MAX235, MAX236, MAX240, and MAX241, all receivers are disabled during shutdown. On the MAX223 and MAX242, two receivers continue to operate in a reduced power mode when the chip is in shutdown. Under these conditions, the propagation delay increases to about 2.5µs for a high-to-low input transition. When in shutdown, the receiver acts as a CMOS inverter with no hysteresis. The MAX223 and MAX242 also have a receiver output enable input (EN for the MAX242 and EN for the MAX223) that allows receiver output control independent of SHDN (SHDN for MAX241). With all other devices, SHDN (SHDN for MAX241) also disables the receiver outputs.

The MAX225 provides five transmitters and five receivers, while the MAX245 provides ten receivers and eight transmitters. Both devices have separate receiver and transmitter-enable controls. The charge pumps turn off and the devices shut down when a logic high is applied to the ENT input. In this state, the supply current drops to less than 25µA and the receivers continue to operate in a low-power receive mode. Driver outputs enter a high-impedance state (three-state mode). On the MAX225, all five receivers are controlled by the ENR input. On the MAX245, eight of the receiver outputs are controlled by the ENR input. On the MAX245, and RB5) are always active. RA1–RA4 and RB1–RB4 are put in a three-state mode when ENR is a logic high.

Receiver and Transmitter Enable Control Inputs

The MAX225 and MAX245–MAX249 feature transmitter and receiver enable controls.

The receivers have three modes of operation: full-speed receive (normal active), three-state (disabled), and low-power receive (enabled receivers continue to function at lower data rates). The receiver enable inputs control the full-speed receive and three-state modes. The transmitters have two modes of operation: full-speed transmit (normal active) and three-state (disabled). The transmitter enable inputs also control the shutdown mode. The device enters shutdown mode when all transmitters are disabled. Enabled receivers function in the low-power receive mode when in shutdown.

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Tables 1a–1d define the control states. The MAX244 has no control pins and is not included in these tables.

The MAX246 has ten receivers and eight drivers with two control pins, each controlling one side of the device. A logic high at the A-side control input ($\overline{\text{ENA}}$) causes the four A-side receivers and drivers to go into a three-state mode. Similarly, the B-side control input ($\overline{\text{ENB}}$) causes the four B-side drivers and receivers to go into a three-state mode. As in the MAX245, one A-side and one B-side receiver (RA5 and RB5) remain active at all times. The entire device is put into shutdown mode when both the A and B sides are disabled ($\overline{\text{ENA}} = \overline{\text{ENB}} = +5\text{V}$).

The MAX247 provides nine receivers and eight drivers with four control pins. The ENRA and ENRB receiver enable inputs each control four receiver outputs. The ENTA and ENTB transmitter enable inputs each control four drivers. The ninth receiver (RB5) is always active. The device enters shutdown mode with a logic high on both ENTA and ENTB.

The MAX248 provides eight receivers and eight drivers with four control pins. The ENRA and ENRB receiver enable inputs each control four receiver outputs. The ENTA and ENTB transmitter enable inputs control four drivers each. This part does not have an always-active receiver. The device enters shutdown mode and transmitters go into a three-state mode with a logic high on both ENTA and ENTB.

The MAX249 provides ten receivers and six drivers with four control pins. The ENRA and ENRB receiver enable inputs each control five receiver outputs. The ENTA and ENTB transmitter enable inputs control three drivers each. There is no always-active receiver. The device enters shutdown mode and transmitters go into a three-state mode with a logic high on both ENTA and ENTB. In shutdown mode, active receivers operate in a low-power receive mode at data rates up to 20kb/s.

Applications Information

Figures 5 through 25 show pin configurations and typical operating circuits. In applications that are sensitive to power-supply noise, V_{CC} should be decoupled to ground with a capacitor of the same value as C1 and C2 connected as close as possible to the device.

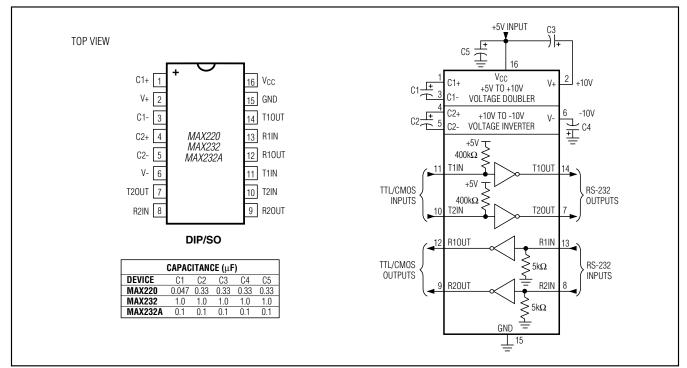


Figure 5. MAX220/MAX232/MAX232A Pin Configuration and Typical Operating Circuit

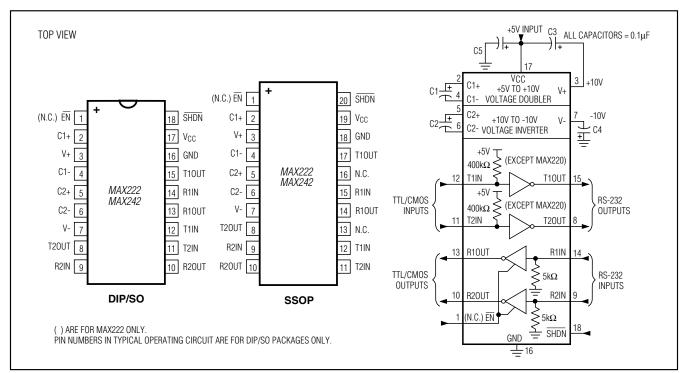


Figure 6. MAX222/MAX242 Pin Configurations and Typical Operating Circuit

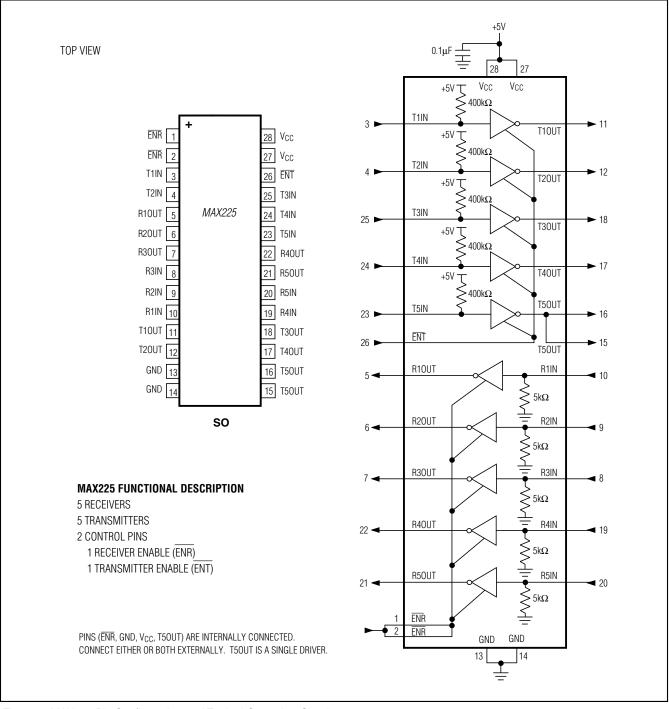


Figure 7. MAX225 Pin Configuration and Typical Operating Circuit

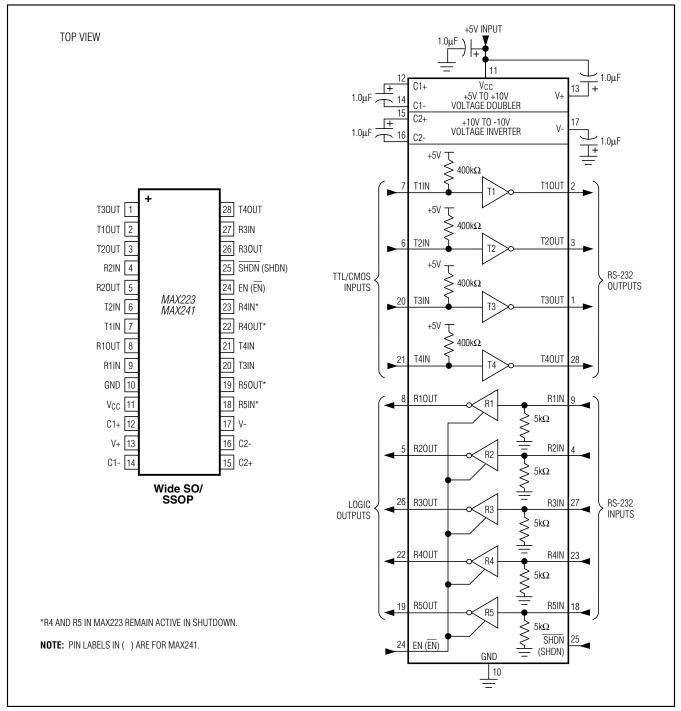


Figure 8. MAX223/MAX241 Pin Configuration and Typical Operating Circuit

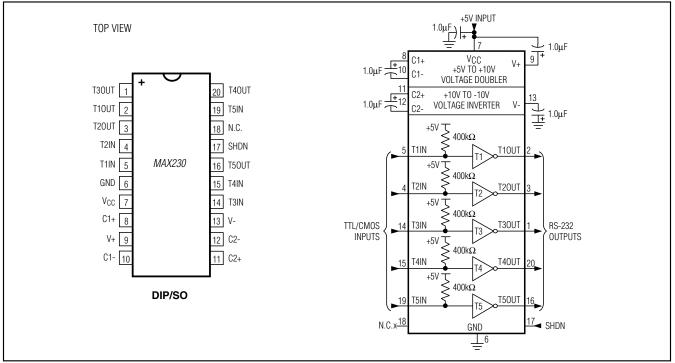


Figure 9. MAX230 Pin Configuration and Typical Operating Circuit

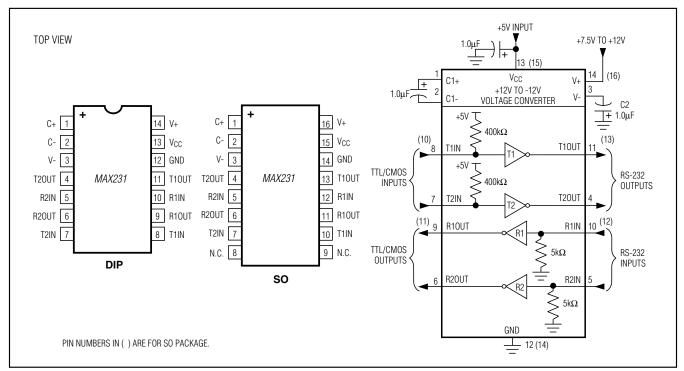


Figure 10. MAX231 Pin Configurations and Typical Operating Circuit

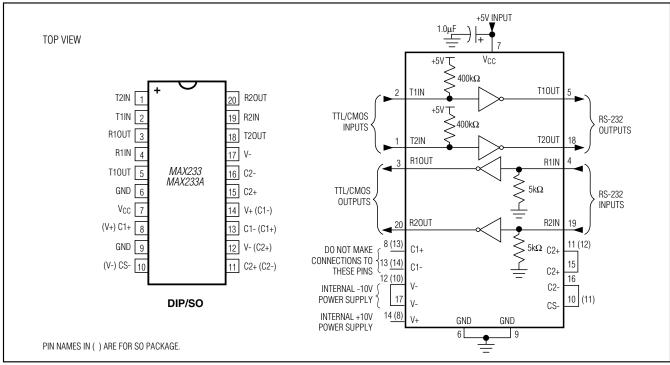


Figure 11. MAX233/MAX233A Pin Configuration and Typical Operating Circuit

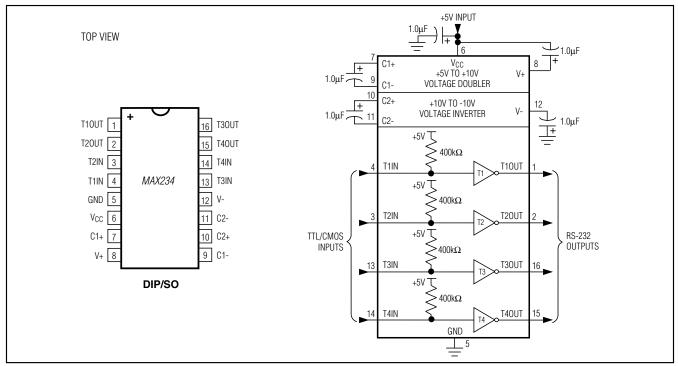


Figure 12. MAX234 Pin Configuration and Typical Operating Circuit

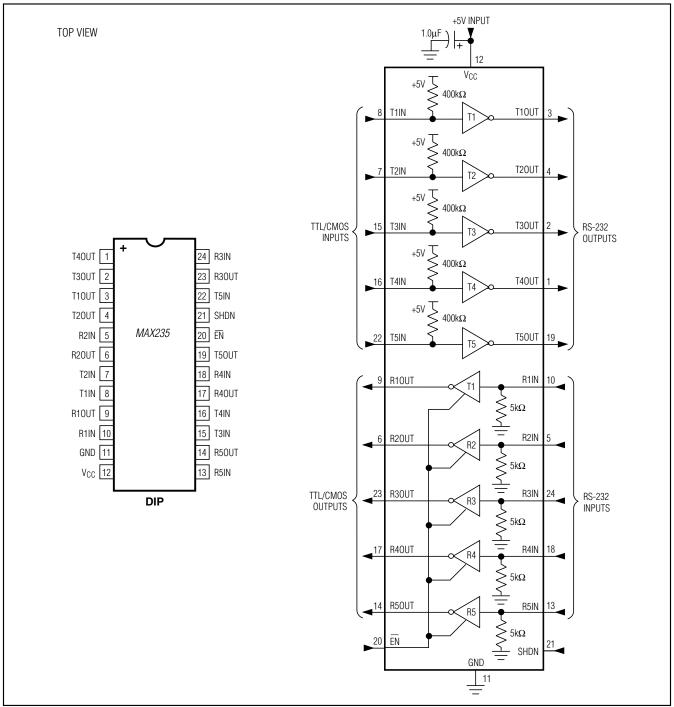


Figure 13. MAX235 Pin Configuration and Typical Operating Circuit

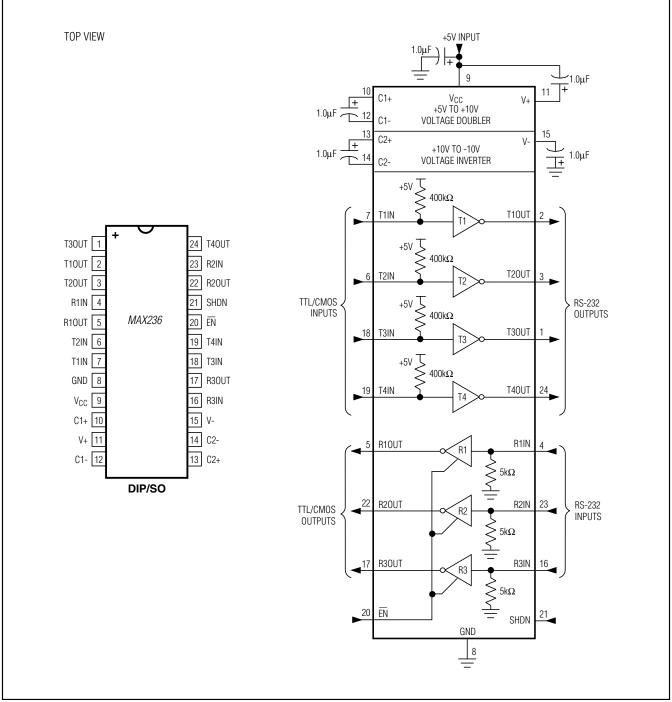


Figure 14. MAX236 Pin Configuration and Typical Operating Circuit

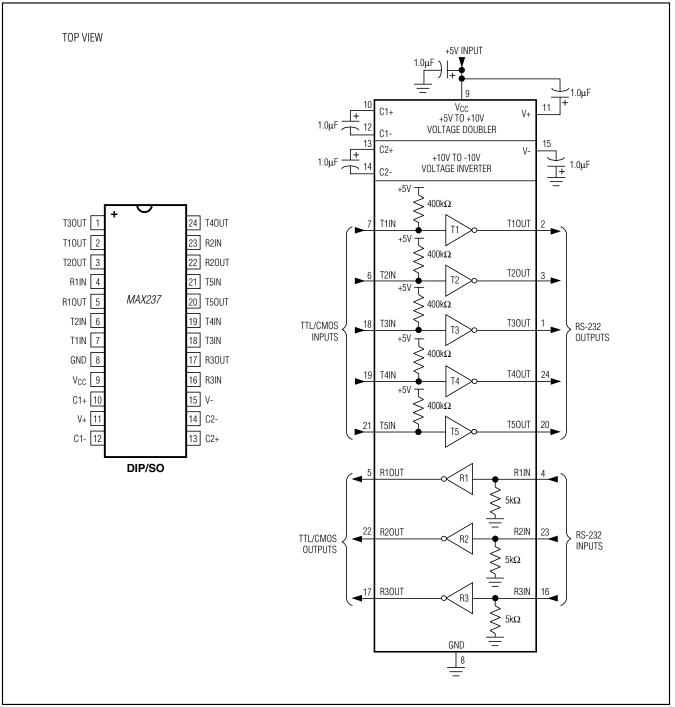


Figure 15. MAX237 Pin Configuration and Typical Operating Circuit

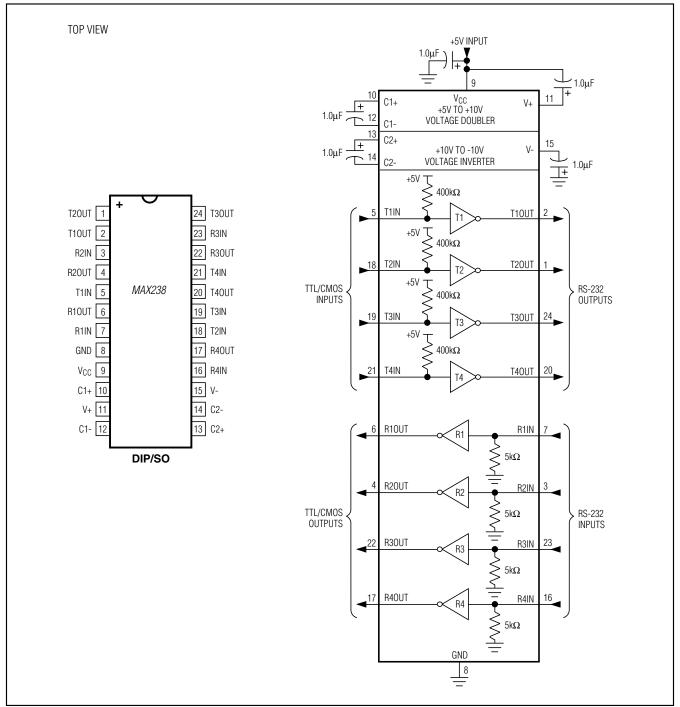


Figure 16. MAX238 Pin Configuration and Typical Operating Circuit

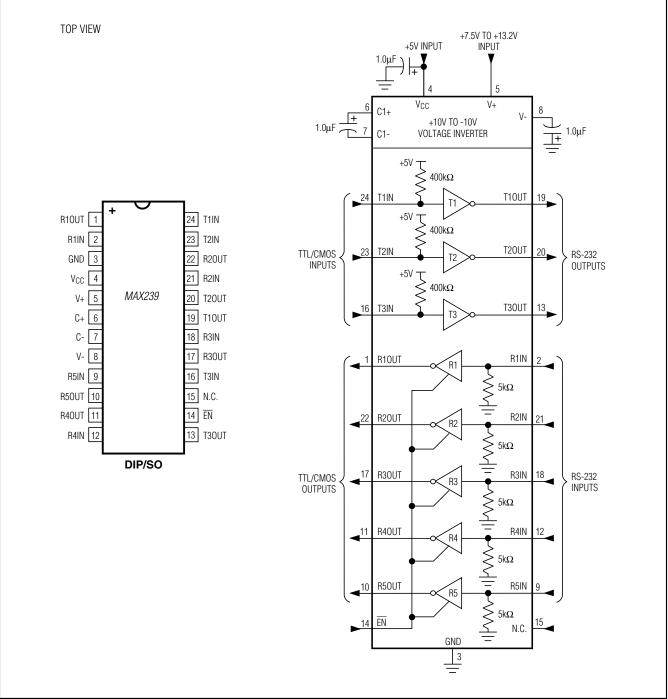


Figure 17. MAX239 Pin Configuration and Typical Operating Circuit

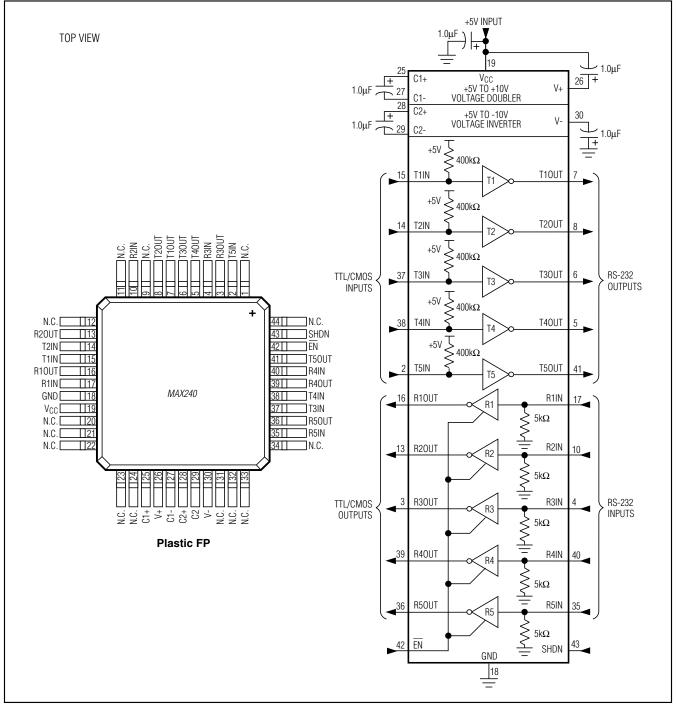


Figure 18. MAX240 Pin Configuration and Typical Operating Circuit

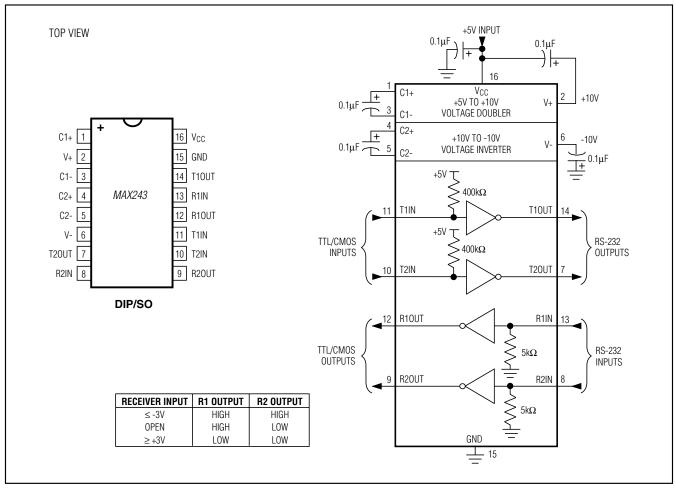


Figure 19. MAX243 Pin Configuration and Typical Operating Circuit

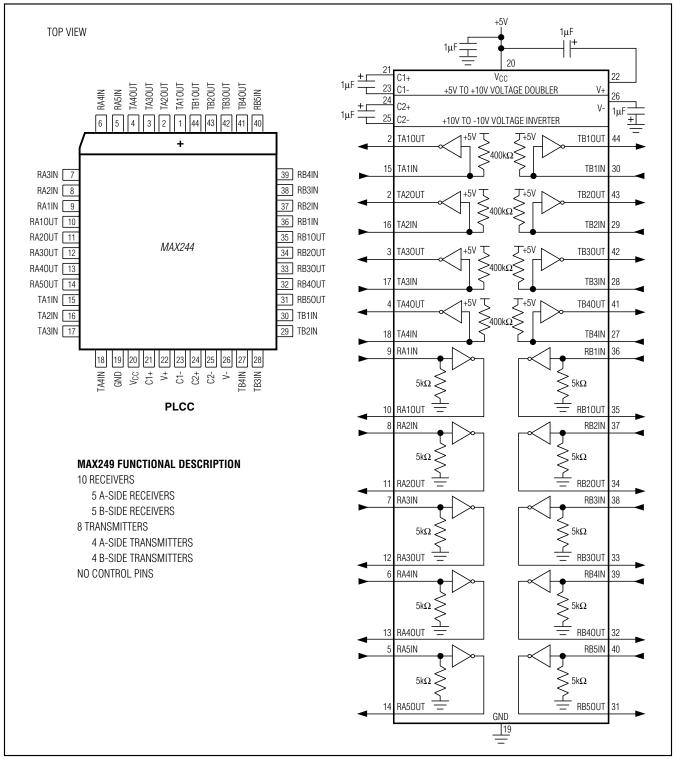


Figure 20. MAX244 Pin Configuration and Typical Operating Circuit

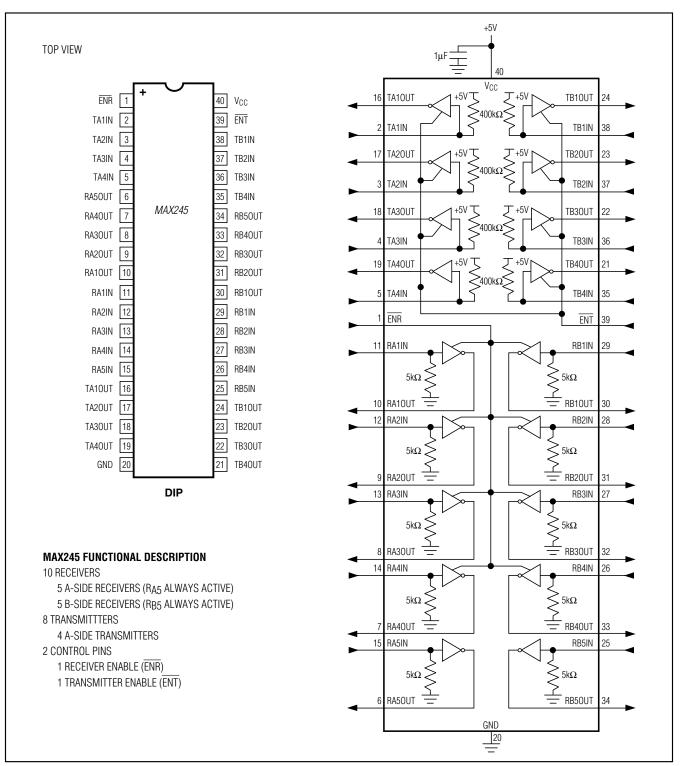


Figure 21. MAX245 Pin Configuration and Typical Operating Circuit

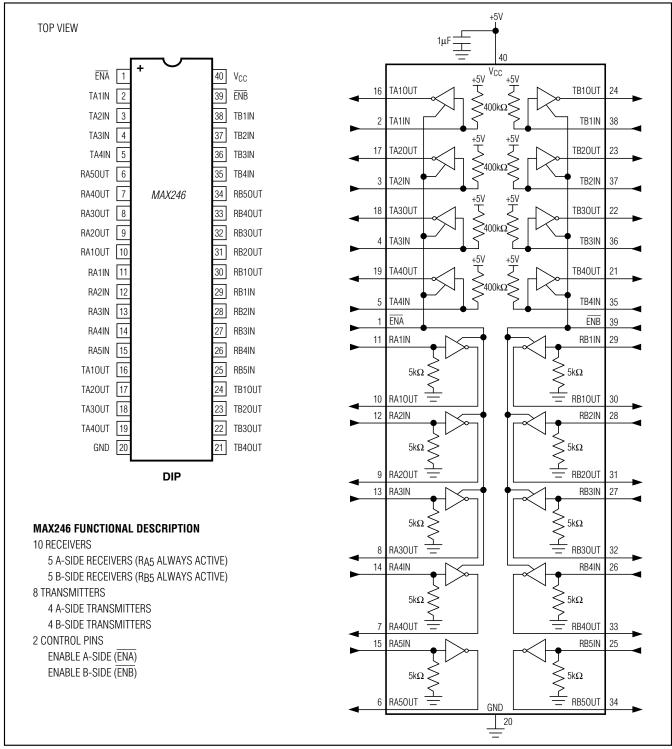


Figure 22. MAX246 Pin Configuration and Typical Operating Circuit

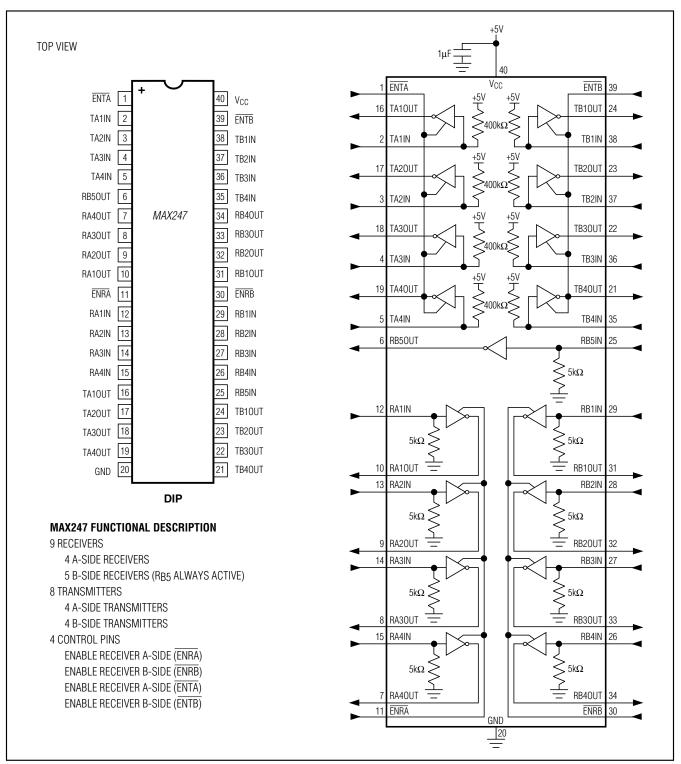


Figure 23. MAX247 Pin Configuration and Typical Operating Circuit

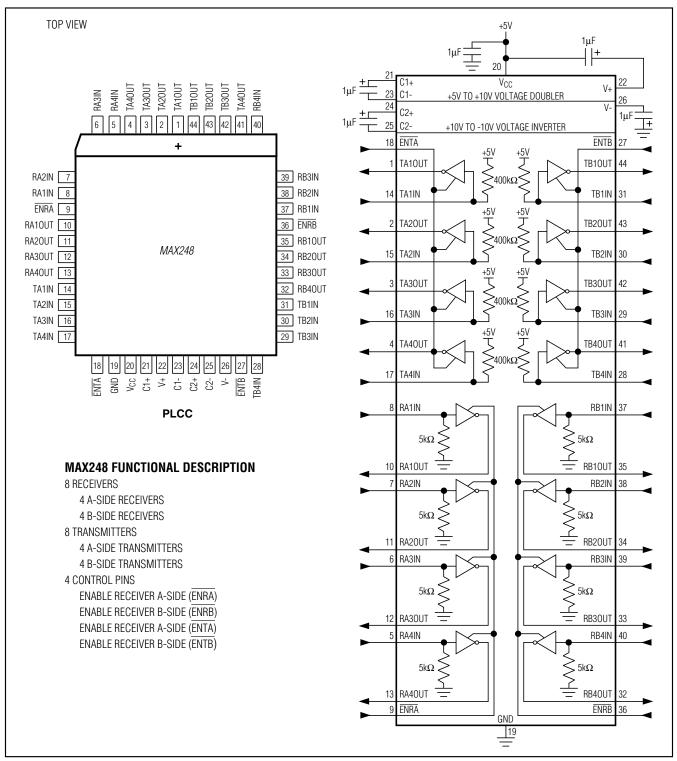


Figure 24. MAX248 Pin Configuration and Typical Operating Circuit

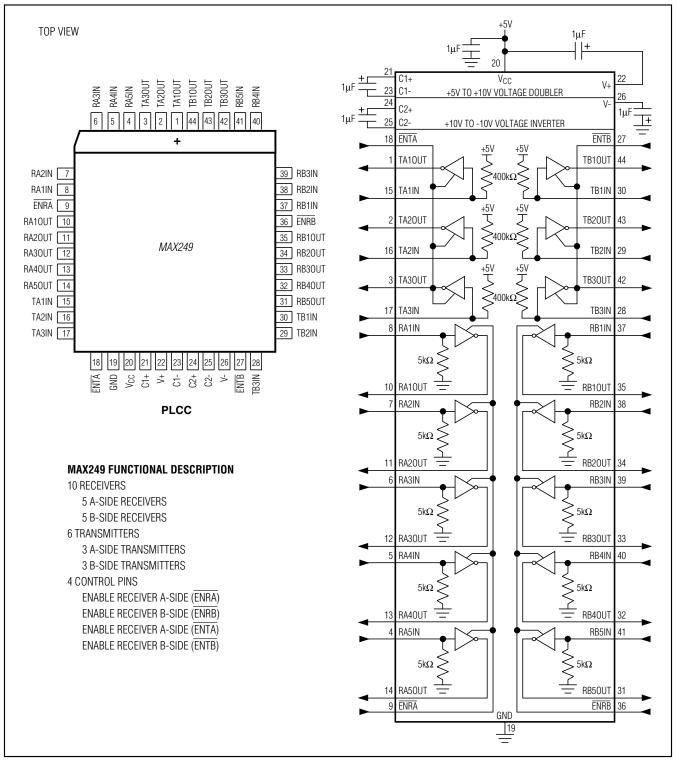


Figure 25. MAX249 Pin Configuration and Typical Operating Circuit

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PART	TEMP RANGE	PIN-PACKAGE
MAX222CPN+	0°C to +70°C	18 Plastic DIP
MAX222CWN+	0°C to +70°C	18 Wide SO
MAX222C/D	0°C to +70°C	Dice*
MAX222EPN+	-40°C to +85°C	18 Plastic DIP
MAX222EWN+	-40°C to +85°C	18 Wide SO
MAX222EJN	-40°C to +85°C	18 CERDIP
MAX222MJN	-55°C to +125°C	18 CERDIP
MAX223CAI+	0°C to +70°C	28 SSOP
MAX223CWI+	0°C to +70°C	28 Wide SO
MAX223C/D	0°C to +70°C	Dice*
MAX223EAI+	-40°C to +85°C	28 SSOP
MAX223EWI+	-40°C to +85°C	28 Wide SO
MAX225CWI+	0°C to +70°C	28 Wide SO
MAX225EWI+	-40°C to +85°C	28 Wide SO
MAX230CPP+	0°C to +70°C	20 Plastic DIP
MAX230CWP+	0°C to +70°C	20 Wide SO
MAX230C/D	0°C to +70°C	Dice*
MAX230EPP+	-40°C to +85°C	20 Plastic DIP
MAX230EWP+	-40°C to +85°C	20 Wide SO
MAX230EJP	-40°C to +85°C	20 CERDIP
MAX230MJP	-55°C to +125°C	20 CERDIP
MAX231CPD+	0°C to +70°C	14 Plastic DIP
MAX231CWE+	0°C to +70°C	16 Wide SO
MAX231CJD	0°C to +70°C	14 CERDIP
MAX231C/D	0°C to +70°C	Dice*
MAX231EPD+	-40°C to +85°C	14 Plastic DIP
MAX231EWE+	-40°C to +85°C	16 Wide SO
MAX231EJD	-40°C to +85°C	14 CERDIP
MAX231MJD	-55°C to +125°C	14 CERDIP
MAX232CPE+	0°C to +70°C	16 Plastic DIP
MAX232CSE+	0°C to +70°C	16 Narrow SO
MAX232CWE+	0°C to +70°C	16 Wide SO
MAX232C/D	0°C to +70°C	Dice*
MAX232EPE+	-40°C to +85°C	16 Plastic DIP
MAX232ESE+	-40°C to +85°C	16 Narrow SO
MAX232EWE+	-40°C to +85°C	16 Wide SO
MAX232EJE	-40°C to +85°C	16 CERDIP
MAX232MJE	-55°C to +125°C	16 CERDIP
MAX232MLP+	-55°C to +125°C	20 LCC
MAX232ACPE+	0°C to +70°C	16 Plastic DIP
MAX232ACSE+	0°C to +70°C	16 Narrow SO
MAX232ACWE+	0°C to +70°C	16 Wide SO

Ordering Information (continued)

PART	TEMP RANGE	PIN-PACKAGE
MAX232AC/D	0°C to +70°C	Dice*
MAX232AEPE+	-40°C to +85°C	16 Plastic DIP
MAX232AESE+	-40°C to +85°C	16 Narrow SO
MAX232AEWE+	-40°C to +85°C	16 Wide SO
MAX232AEJE	-40°C to +85°C	16 CERDIP
MAX232AMJE	-55°C to +125°C	16 CERDIP
MAX232AMLP+	-55°C to +125°C	20 LCC
MAX233CPP+	0°C to +70°C	20 Plastic DIP
MAX233EPP+	-40°C to +85°C	20 Plastic DIP
MAX233ACPP+	0°C to +70°C	20 Plastic DIP
MAX233ACWP+	0°C to +70°C	20 Wide SO
MAX233AEPP+	-40°C to +85°C	20 Plastic DIP
MAX233AEWP+	-40°C to +85°C	20 Wide SO
MAX234CPE+	0°C to +70°C	16 Plastic DIP
MAX234CWE+	0°C to +70°C	16 Wide SO
MAX234C/D	0°C to +70°C	Dice*
MAX234EPE+	-40°C to +85°C	16 Plastic DIP
MAX234EWE+	-40°C to +85°C	16 Wide SO
MAX234EJE	-40°C to +85°C	16 CERDIP
MAX234MJE	-55°C to +125°C	16 CERDIP
MAX235CPG+	0°C to +70°C	24 Wide Plastic DIP
MAX235EPG+	-40°C to +85°C	24 Wide Plastic DIP
MAX235EDG	-40°C to +85°C	24 Ceramic SB
MAX235MDG	-55°C to +125°C	24 Ceramic SB
MAX236CNG+	0°C to +70°C	24 Narrow Plastic DIP
MAX236CWG+	0°C to +70°C	24 Wide SO
MAX236C/D	0°C to +70°C	Dice*
MAX236ENG+	-40°C to +85°C	24 Narrow Plastic DIP
MAX236EWG+	-40°C to +85°C	24 Wide SO
MAX236ERG	-40°C to +85°C	24 Narrow CERDIP
MAX236MRG	-55°C to +125°C	24 Narrow CERDIP
MAX237CNG+	0°C to +70°C	24 Narrow Plastic DIP
MAX237CWG+	0°C to +70°C	24 Wide SO
MAX237C/D	0°C to +70°C	Dice*
MAX237ENG+	-40°C to +85°C	24 Narrow Plastic DIP
MAX237EWG+	-40°C to +85°C	24 Wide SO
MAX237ERG	-40°C to +85°C	24 Narrow CERDIP
MAX237MRG	-55°C to +125°C	24 Narrow CERDIP
MAX238CNG+	0°C to +70°C	24 Narrow Plastic DIP
MAX238CWG+	0°C to +70°C	24 Wide SO
MAX238C/D	0°C to +70°C	Dice*

+Denotes a lead(Pb)-free/RoHS-compliant package.

*Contact factory for dice specifications.

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PART	TEMP RANGE	PIN-PACKAGE
MAX238ENG+	-40°C to +85°C	24 Narrow Plastic DIP
MAX238EWG+	-40°C to +85°C	24 Wide SO
MAX238ERG	-40°C to +85°C	24 Narrow CERDIP
MAX238MRG	-55°C to +125°C	24 Narrow CERDIP
MAX239CNG+	0°C to +70°C	24 Narrow Plastic DIP
MAX239CWG+	0°C to +70°C	24 Wide SO
MAX239C/D	0°C to +70°C	Dice*
MAX239ENG+	-40°C to +85°C	24 Narrow Plastic DIP
MAX239EWG+	-40°C to +85°C	24 Wide SO
MAX239ERG	-40°C to +85°C	24 Narrow CERDIP
MAX239MRG	-55°C to +125°C	24 Narrow CERDIP
MAX240CMH+	0°C to +70°C	44 Plastic FP
MAX240C/D	0°C to +70°C	Dice*
MAX241CAI+	0°C to +70°C	28 SSOP
MAX241CWI+	0°C to +70°C	28 Wide SO
MAX241C/D	0°C to +70°C	Dice*
MAX241EAI+	-40°C to +85°C	28 SSOP
MAX241EWI+	-40°C to +85°C	28 Wide SO
MAX242CAP+	0°C to +70°C	20 SSOP
MAX242CPN+	0°C to +70°C	18 Plastic DIP
MAX242CWN+	0°C to +70°C	18 Wide SO
MAX242C/D	0°C to +70°C	Dice*
MAX242EPN+	-40°C to +85°C	18 Plastic DIP
MAX242EWN+	-40°C to +85°C	18 Wide SO
MAX242EJN	-40°C to +85°C	18 CERDIP
MAX242MJN	-55°C to +125°C	18 CERDIP

Ordering Information (continued)

PART	TEMP RANGE	PIN-PACKAGE
MAX243CPE+	0°C to +70°C	16 Plastic DIP
MAX243CSE+	0°C to +70°C	16 Narrow SO
MAX243CWE+	0°C to +70°C	16 Wide SO
MAX243C/D	0°C to +70°C	Dice*
MAX243EPE+	-40°C to +85°C	16 Plastic DIP
MAX243ESE+	-40°C to +85°C	16 Narrow SO
MAX243EWE+	-40°C to +85°C	16 Wide SO
MAX243EJE	-40°C to +85°C	16 CERDIP
MAX243MJE	-55°C to +125°C	16 CERDIP
MAX244CQH+	0°C to +70°C	44 PLCC
MAX244C/D	0°C to +70°C	Dice*
MAX244EQH+	-40°C to +85°C	44 PLCC
MAX245CPL+	0°C to +70°C	40 Plastic DIP
MAX245C/D	0°C to +70°C	Dice*
MAX245EPL+	-40°C to +85°C	40 Plastic DIP
MAX246CPL+	0°C to +70°C	40 Plastic DIP
MAX246C/D	0°C to +70°C	Dice*
MAX246EPL+	-40°C to +85°C	40 Plastic DIP
MAX247CPL+	0°C to +70°C	40 Plastic DIP
MAX247C/D	0°C to +70°C	Dice*
MAX247EPL+	-40°C to +85°C	40 Plastic DIP
MAX248CQH+	0°C to +70°C	44 PLCC
MAX248C/D	0°C to +70°C	Dice*
MAX248EQH+	-40°C to +85°C	44 PLCC
MAX249CQH+	0°C to +70°C	44 PLCC
MAX249EQH+	-40°C to +85°C	44 PLCC

+Denotes a lead(Pb)-free/RoHS-compliant package.

*Contact factory for dice specifications.

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Package Information

For the latest package outline information and land patterns, go to <u>www.maxim-ic.com/packages</u>. Note that a "+", "#", or "-" in the package code indicates RoHS status only. Package drawings may show a different suffix character, but the drawing pertains to the package regardless of RoHS status.

PACKAGE TYPE	PACKAGE CODE	OUTLINE NO.	LAND PATTERN NO.	
14 PDIP	P14+3			
16 PDIP	P16+1			
16 PDIP	P16+2			
16 PDIP	P16+3	<u>21-0043</u>		
18 PDIP	P18+5			
20 PDIP	P20+3			
20 PDIP	P20M+1			
24 PDIP	N24+3			
24 PDIP	P24M+1			
28 PDIP	P28+2			
40 PDIP	P40+1	<u>21-0044</u>		
40 PDIP	P40M+2			
14 CERDIP	J14-3			
16 CERDIP	J16-3	<u>21-0045</u>		
18 CERDIP	J18-2			
20 CERDIP	J20-2			
24 CERDIP	R24-4			
16 SO(N)	S16+3		<u>90-0097</u>	
16 SO(N)	S16+5	<u>21-0041</u>		
16 SO(W)	W16+1		<u>90-0107</u>	
16 SO(W)	W16+2			
16 SO(W)	W16+3			
18 SO(W)	W18+1		<u>90-0181</u>	
20 SO(W)	W20+3		<u>90-0108</u>	
20 SO(W)	W20M+1	<u>21-0042</u>		
24 SO(W)	W24+2		<u>90-0182</u>	
28 SO(W)	W28+1		<u>90-0109</u>	
28 SO(W)	W28+2			
28 SO(W)	W28M+1			
20 LCC	L20+3	<u>21-0658</u>	<u>90-0177</u>	
20 SSOP	A20+1	21-0056	<u>90-0094</u>	
24 SSOP	A24+2		<u>90-0110</u>	
28 SSOP	A28+1		<u>90-0095</u>	
16 TSSOP	U16+1		<u>90-0117</u>	
16 FPCK	F16-3	<u>21-0013</u>	_	
44 MQFP	M44+5	<u>21-0826</u>	<u>90-0169</u>	
44 PLCC	Q44+1			
44 PLCC	Q44+2	<u>21-0049</u>	<u>90-0236</u>	

+5V-Powered, Multichannel RS-232 Drivers/Receivers

Revision History

REVISION NUMBER	REVISION DATE	DESCRIPTION	PAGES CHANGED
15	1/06	Added part information to the lead temperature in the Absolute Maximum Ratings sections	2, 5, 8
16	7/10	Changed multiple packages to lead-free versions; updated/added notes 3, 4, 5, 7, and 8 to the <i>Electrical Characteristics</i> table; removed incorrect subscripting from all pin names in the <i>Electrical Characteristics</i> table and <i>Pin Configurations</i>	1, 2–9, 17–36



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